RV1106G_GMF_V1.0

Reference Design Main Functions Introduction	
Power	Discrete
RAM	SPI FLASH
Interface	SDMMC/SDIO/MIPI_CSI/I2S/USB/ADC

HINLINK

Project:	**SoloLinker-A**	HINLINK	
File:	**01.Cover Page**		
Date:	Wednesday, March 20, 2024	Rev:	<Revision>
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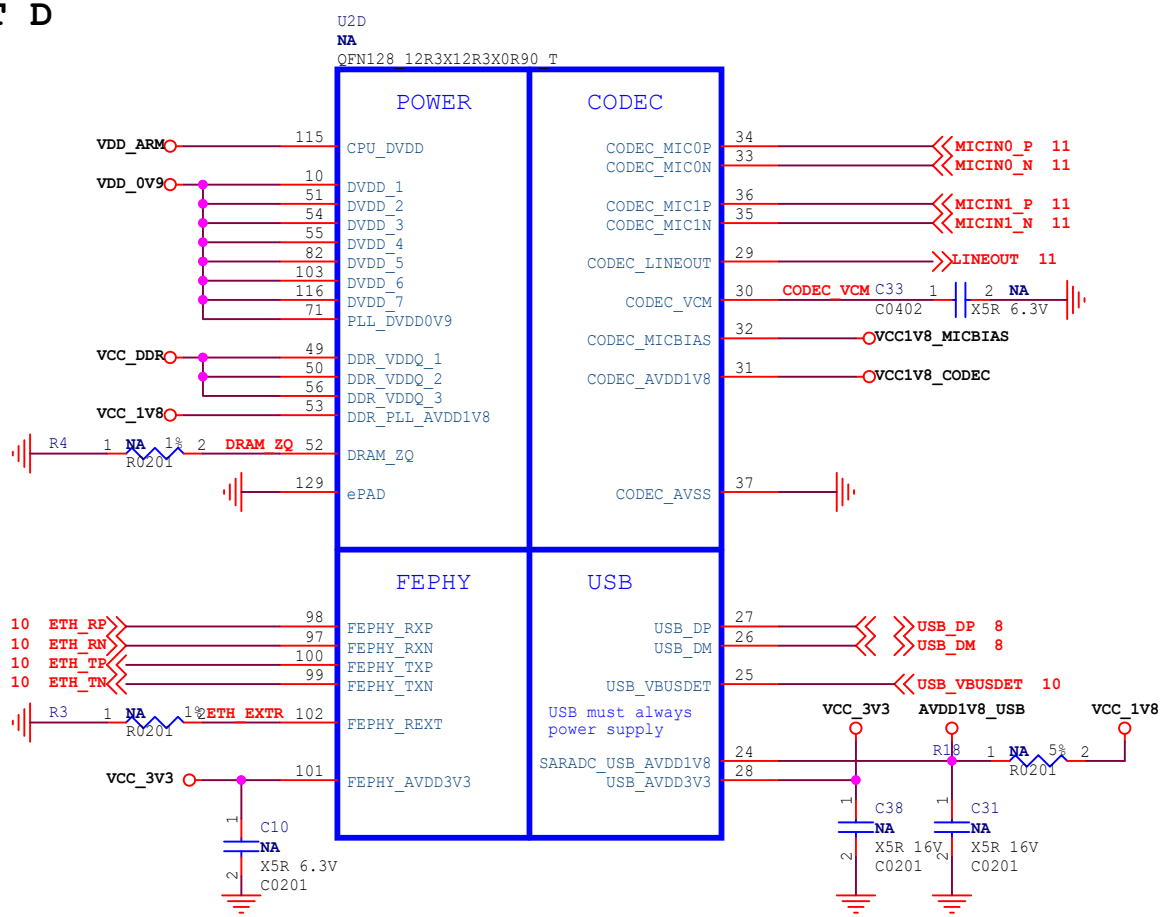
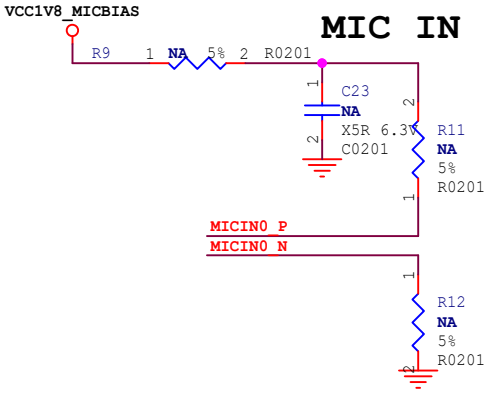
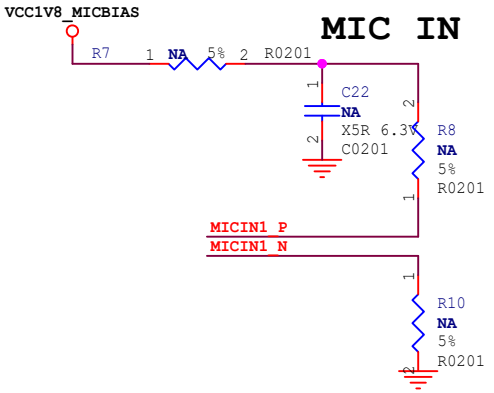
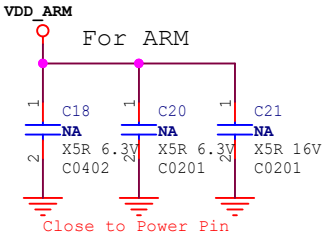
PART D

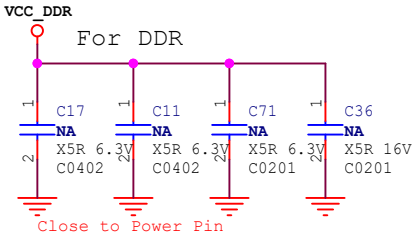


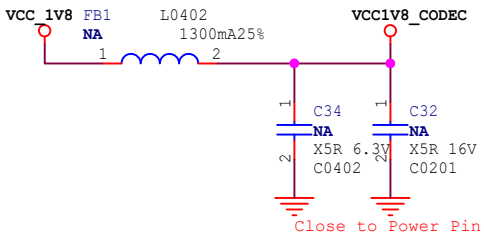


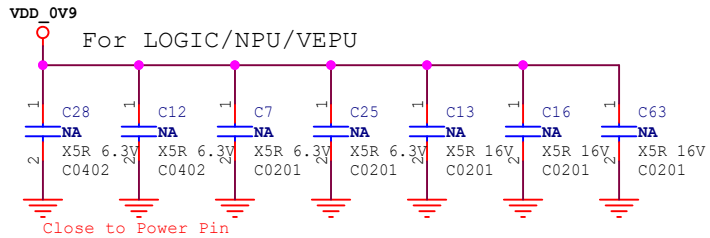


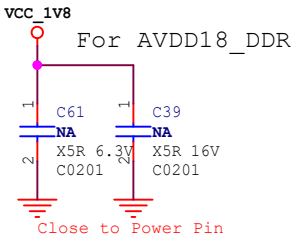
POWER











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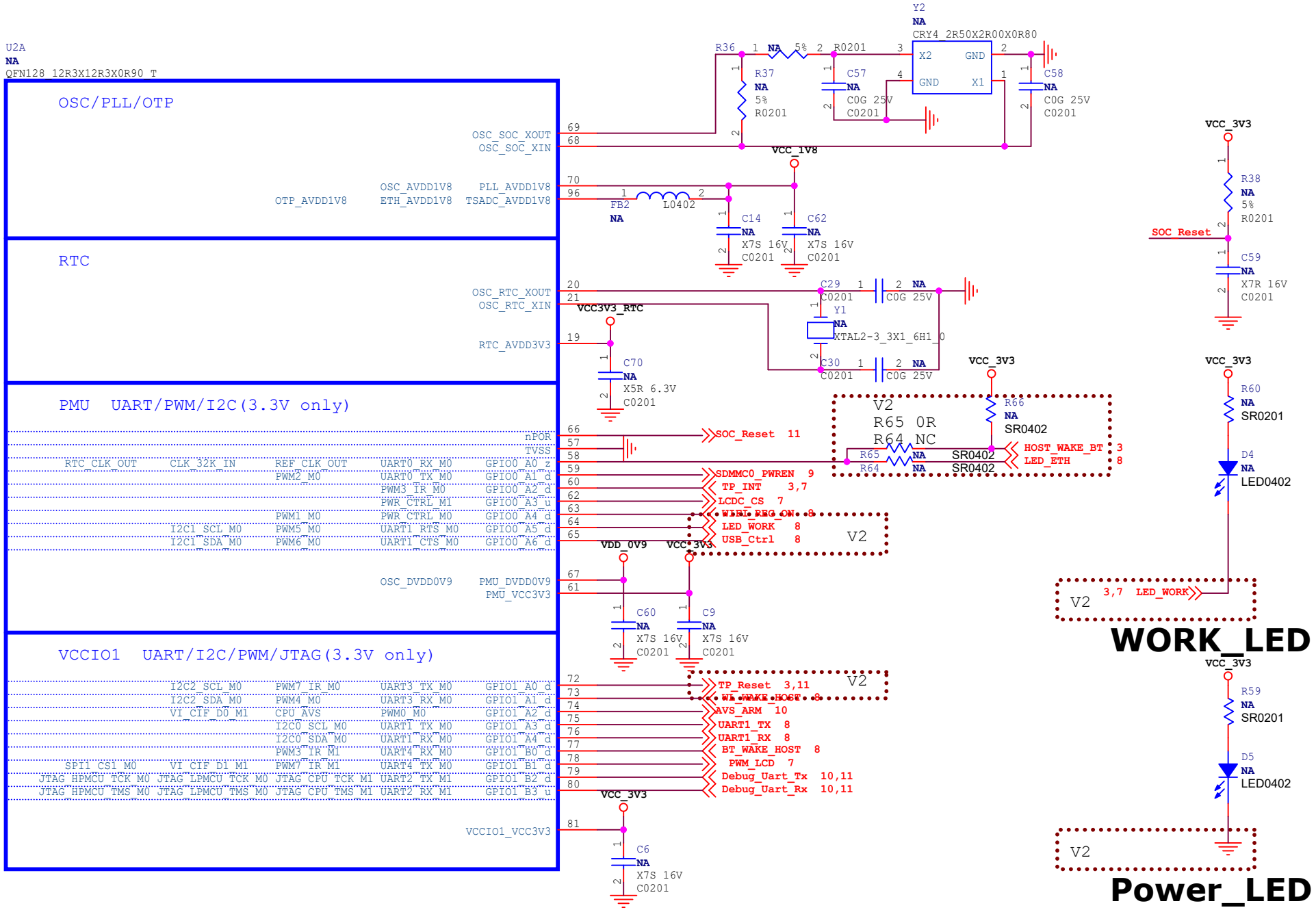
Designer:	<designer>	Sheet:	2 of 12

PART A



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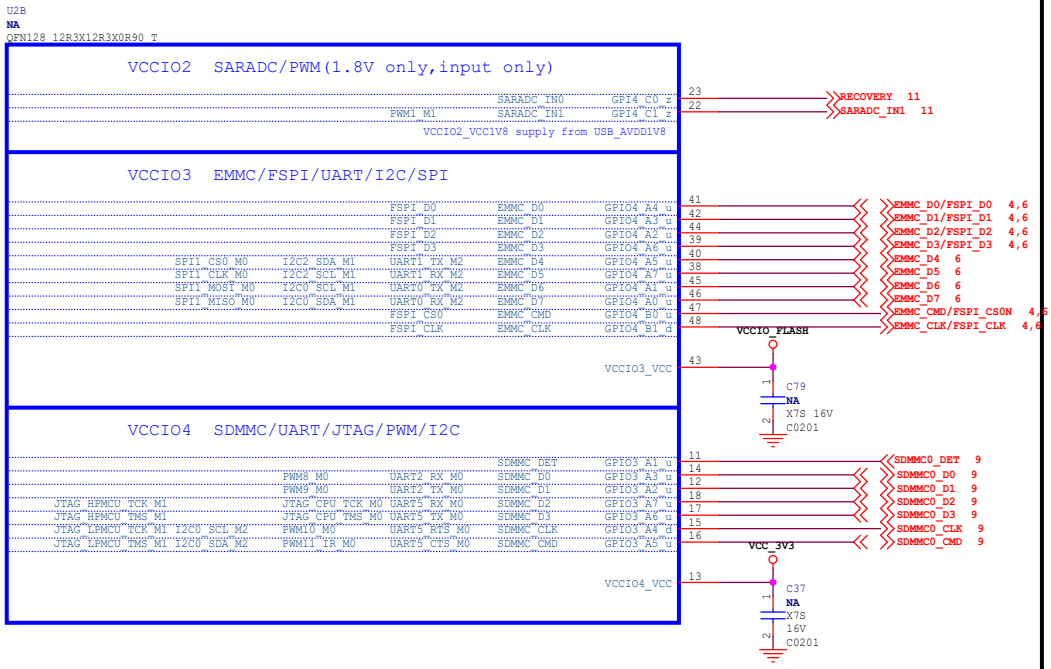
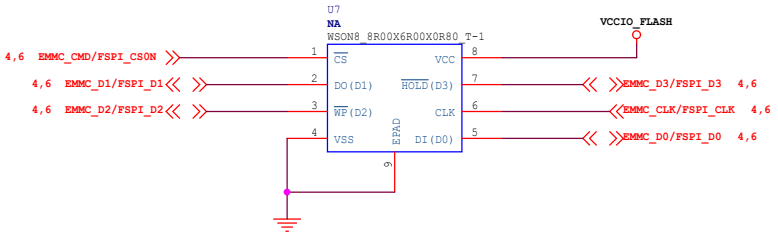
File:	03.RV1106G OSC/RTC/PMU/VCCIO		
Date:	Wednesday, March 20, 2024	Rev:	<Revision>
Designer:	<designer>	Sheet:	3 of 12

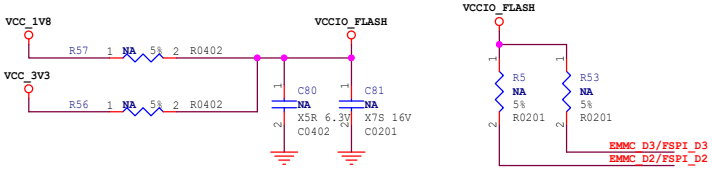
PART B



SPI Flash

NOTE:
Refer to the latest AVL for parts selection.





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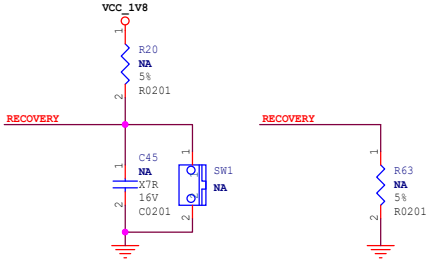

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Date:	Wednesday, March 20, 2024	Rev:	<Revision>
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PART C

U2C
NA
QFN128_12R3X12R3X0R90_T

VCCIO5 SDIO/I2S/VO_LCDC/UART/PWM/I2C

	I2C4_SCL_M0	UART1_RTS_M1	VO_LCDC_D9	I2S0_LRCK	SDIO_D0_M0	GPIO2_A1_d	110
	I2C4_SDA_M0	UART1_CTS_M1	VO_LCDC_D8	I2S0_SCLK	SDIO_D1_M0	GPIO2_A0_d	109
		UART1_RX_M1	VO_LCDC_D13	I2S0_SDI0	SDIO_D2_M0	GPIO2_A5_d	114
		UART1_TX_M1	VO_LCDC_D12	I2S0_SDO0	SDIO_D3_M0	GPIO2_A4_d	113
			VO_LCDC_D10	I2S0_MCLK	SDIO_CLK_M0	GPIO2_A2_d	111
			VO_LCDC_D11	I2S0_SDO3_SDI1	SDIO_CMD_M0	GPIO2_A3_d	112
FLASH_TRIGOUT	I2C3_SCL_M0	PWM2_M1	VO_LCDC_D14	I2S0_SDO2_SDI2	UART0_RTS_M1	GPIO2_A6_d	107
PRELIGHT_TRIGOUT	I2C3_SDA_M0	PWM4_M1	VO_LCDC_D15	I2S0_SDO1_SDI3	UART0_CTS_M1	GPIO2_A7_d	106
		PWM5_M1	VO_LCDC_D16	I2C1_SCL_M1	UART0_RX_M1	GPIO2_B0_d	105
		PWM6_M1	VO_LCDC_D17	I2C1_SDA_M1	UART0_TX_M1	GPIO2_B1_d	104

LCDC_D9 7
LCDC_D8 7
LCDC_D13 7
LCDC_D12 7
LCDC_D10 7
LCDC_D11 7
LCDC_D14 7
LCDC_D15 7
LCDC_D16 7
LCDC_D17 7

VCC_3V3
VCCIO5_VCC 108
C15 NA X5R 16V C0201

VCCIO6 VO_LCDC/VI_CIF/PWM/UART/I2C/SDIO/SPI/AUD_DSM/VI_BT656

VI_BT656_D0_M1		SDIO_D1_M1	SPI0_CS0_M0	PWM2_M2	VI_CIF_D2_M1	VO_LCDC_D7	GPIO1_C0_d	95
VI_BT656_D1_M1		SDIO_D0_M1	SPI0_CLK_M0	PWM4_M2	VI_CIF_D3_M1	VO_LCDC_D6	GPIO1_C1_d	94
VI_BT656_D2_M1	I2C4_SCL_M1	SDIO_CLK_M1	SPI0_MOSI_M0	PWM5_M2	VI_CIF_D4_M1	VO_LCDC_D5	GPIO1_C2_d	93
VI_BT656_D3_M1	I2C4_SDA_M1	SDIO_CMD_M1	SPI0_MISO_M0	PWM6_M2	VI_CIF_D5_M1	VO_LCDC_D4	GPIO1_C3_d	92
VI_BT656_D4_M1		SDIO_D3_M1	UART4_RX_M1	PWM8_M1	VI_CIF_D6_M1	VO_LCDC_D3	GPIO1_C4_d	91
VI_BT656_D5_M1		SDIO_D2_M1	UART4_TX_M1	PWM9_M1	VI_CIF_D7_M1	VO_LCDC_D2	GPIO1_C5_d	90
VI_BT656_D6_M1			UART4_RTS_M1	PWM10_M1	VI_CIF_D8_M1	VO_LCDC_D1	GPIO1_C6_d	83
VI_BT656_D7_M1			UART4_CTS_M1	PWM11_IR_M1	VI_CIF_D9_M1	VO_LCDC_D0	GPIO1_C7_d	84
VI_BT656_CLK_M1		UART3_TX_M1	UART5_RTS_M1	PWM3_IR_M2	VI_CIF_CLKI_M1	VO_LCDC_DEN	GPIO1_D0_d	89
		UART3_RX_M1	UART5_CTS_M1	PWM10_M2	VI_CIF_HREF_M1	VO_LCDC_HSYNC	GPIO1_D1_d	87
SPI0_CS1_M0	I2C3_SDA_M1	AUD_DSM_P	UART5_RX_M1	PWM0_M1	VI_CIF_VSYNC_M1	VO_LCDC_VSYNC	GPIO1_D2_d	86
	I2C3_SCL_M1	AUD_DSM_N	UART5_TX_M1	PWM11_IR_M2	VI_CIF_CLKO_M1	VO_LCDC_CLK	GPIO1_D3_d	85

LCDC_D7 7
LCDC_D6 7
LCDC_D5 7
LCDC_D4 7
LCDC_D3 7
LCDC_D2 7
LCDC_D1 7
LCDC_D0 7
LCDC_DEN 7
LCDC_HSYNC 7
LCDC_VSYNC 7
LCDC_CLK 7

VCC_3V3
VCCIO6_VCC 88
C8 NA X5R 16V C0201

VCCIO7 VI_CIF/MIPI_CSI/LVDS_RX/UART/I2C/VI_BT1120/VI_BT656 (1.8V only,some for input only)

		VI_BT1120_D0	LVDS_RX_D3N	MIPI_CSI_RX_D3N	VI_CIF_D0_M0	GPIO3_B0_d	117
		VI_BT1120_D1	LVDS_RX_D3P	MIPI_CSI_RX_D3P	VI_CIF_D1_M0	GPIO3_B1_d	118
		VI_BT1120_D2	LVDS_RX_CLK1N	MIPI_CSI_RX_CLK1N	VI_CIF_D2_M0	GPIO3_B2_d	119
		VI_BT1120_D3	LVDS_RX_CLK1P	MIPI_CSI_RX_CLK1P	VI_CIF_D3_M0	GPIO3_B3_d	120
		VI_BT1120_D4	LVDS_RX_D2N	MIPI_CSI_RX_D2N	VI_CIF_D4_M0	GPIO3_B4_d	121
		VI_BT1120_D5	LVDS_RX_D2P	MIPI_CSI_RX_D2P	VI_CIF_D5_M0	GPIO3_B5_d	122
		VI_BT1120_D6	LVDS_RX_D1N	MIPI_CSI_RX_D1N	VI_CIF_D6_M0	GPIO3_B6_d	123
		VI_BT1120_D7	LVDS_RX_D1P	MIPI_CSI_RX_D1P	VI_CIF_D7_M0	GPIO3_B7_d	124
	VI_BT656_D0_M0	VI_BT1120_D8	LVDS_RX_CLK0N	MIPI_CSI_RX_CLK0N	VI_CIF_D8_M0	GPIO3_C0_d	125
	VI_BT656_D1_M0	VI_BT1120_D9	LVDS_RX_CLK0P	MIPI_CSI_RX_CLK0P	VI_CIF_D9_M0	GPIO3_C1_d	126
	VI_BT656_CLK_M0	VI_BT1120_CLK	LVDS_RX_D0N	MIPI_CSI_RX_D0N	VI_CIF_CLKI_M0	GPIO3_C2_d	127
			LVDS_RX_D0P	MIPI_CSI_RX_D0P	VI_CIF_HREF_M0	GPIO3_C3_d	128
			MIPI_CLK0_OUT		VI_CIF_CLKO_M0	GPIO3_C4_d	2
					VI_CIF_VSYNC_M0	GPIO3_C5_d	3
	VI_BT656_D2_M0	VI_BT1120_D10	PWM7_IR_M2	MIPI_CLK1_OUT	VI_CIF_D10	GPIO3_C6_d	4
	VI_BT656_D3_M0	VI_BT1120_D11	UART5_TX_M2	I2C4_SCL_M2	VI_CIF_D11	GPIO3_C7_d	5
	VI_BT656_D4_M0	VI_BT1120_D12	UART5_RX_M2	I2C4_SDA_M2	VI_CIF_D12	GPIO3_D0_d	7
	VI_BT656_D5_M0	VI_BT1120_D13	UART5_RTS_M2	I2C3_SCL_M2	VI_CIF_D13	GPIO3_D1_d	6
	VI_BT656_D6_M0	VI_BT1120_D14	UART5_CTS_M2	I2C3_SDA_M2	VI_CIF_D14	GPIO3_D2_d	8
	VI_BT656_D7_M0	VI_BT1120_D15		PWM1_M2	VI_CIF_D15	GPIO3_D3_d	9

MIPI_CLK0 R49 1 NA 5% 2 R0201
MIPI_CLK1 R48 1 NA 5% 2 R0201

MIPI/LVDS_D3N 7
MIPI/LVDS_D3P 7
MIPI/LVDS_CLK1N 7
MIPI/LVDS_CLK1P 7
MIPI/LVDS_D2N 7
MIPI/LVDS_D2P 7
MIPI/LVDS_D1N 7
MIPI/LVDS_D1P 7
MIPI/LVDS_CLK0N 7
MIPI/LVDS_CLK0P 7
MIPI/LVDS_D0N 7
MIPI/LVDS_D0P 7
MIPI/LVDS_MCLK0 7
MIPI/LVDS_RST0 7
MIPI/LVDS_MCLK1 7
MIPI_I2C4_SCL 7,11
MIPI_I2C4_SDA 7,11
MIPI_I2C3_SCL 7,11
MIPI_I2C3_SDA 7,11
MIPI/LVDS_RST1 7

MIPI_AVDD1V8 VCCIO7_VCC1V8
1 MIPI_CSI_AVDD_1V8 R50 1 NA 5% 2 R0201 VCC_1V8
C24 NA X5R 16V C0201

VCC_1V8
R47 NA 5% R0201
R45 NA 5% R0201
MIPI_I2C4_SCL
MIPI_I2C4_SDA

VCC_1V8
R46 NA 5% R0201
R44 NA 5% R0201
MIPI_I2C3_SCL
MIPI_I2C3_SDA

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Date:	Wednesday, March 20, 2024	Rev:	<Revision>
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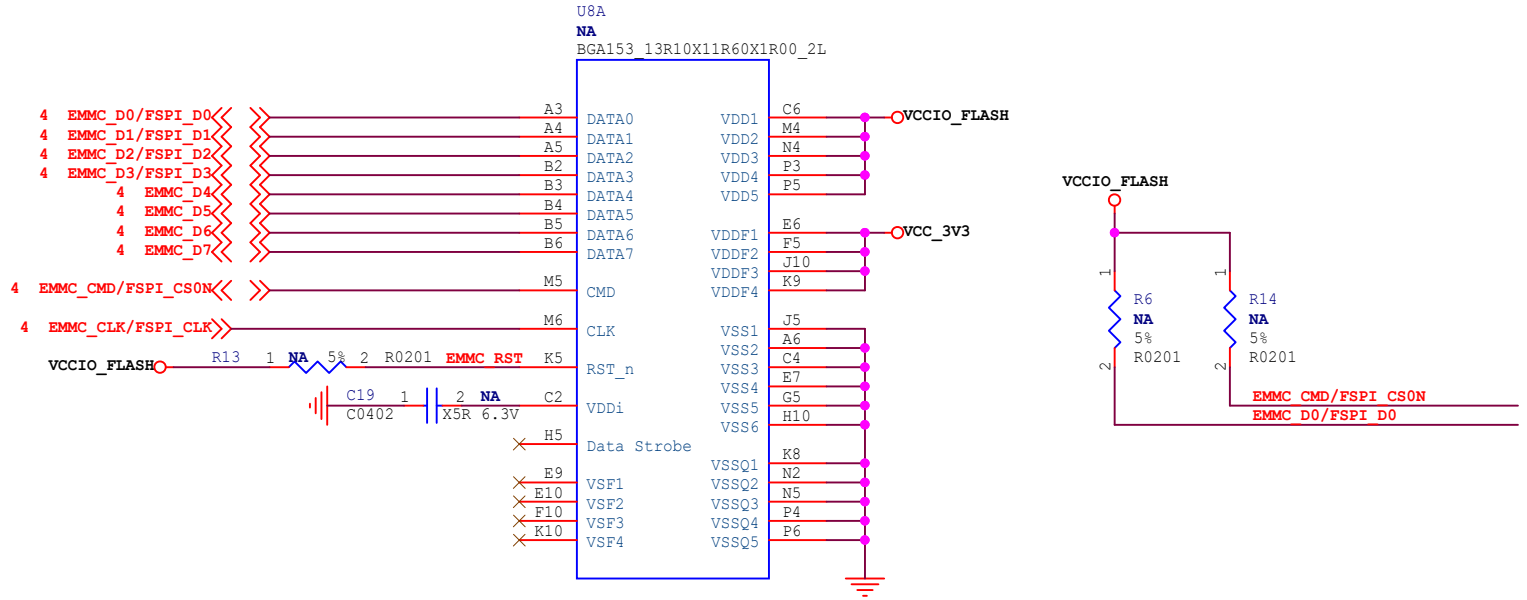
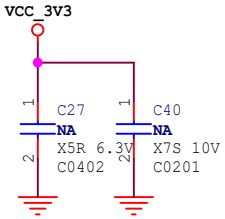
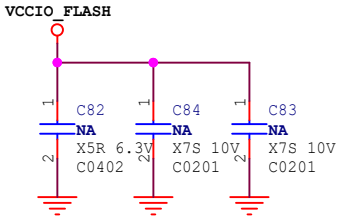
eMMC

NOTE:
Refer to the latest AVL for parts selection.







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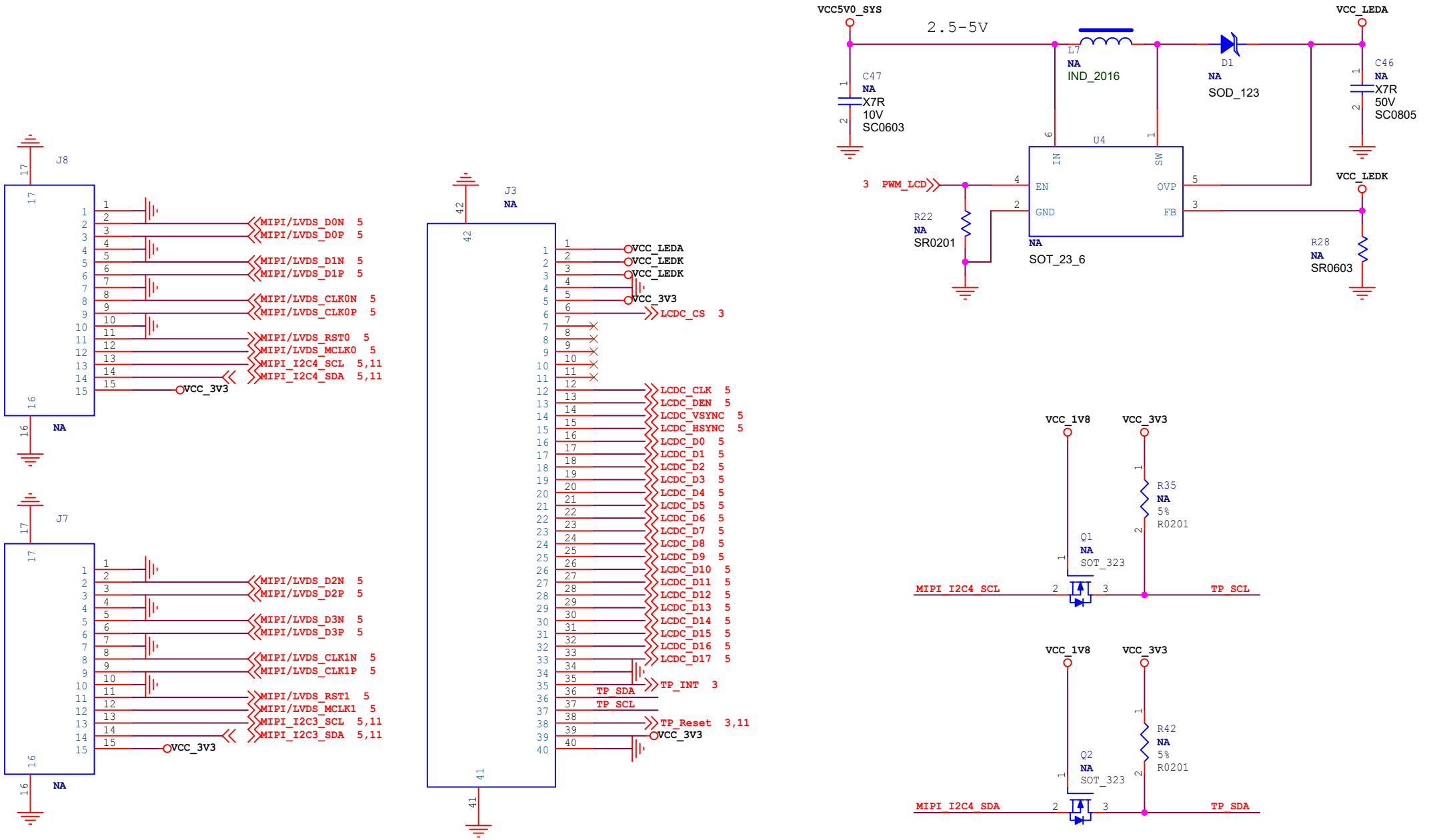

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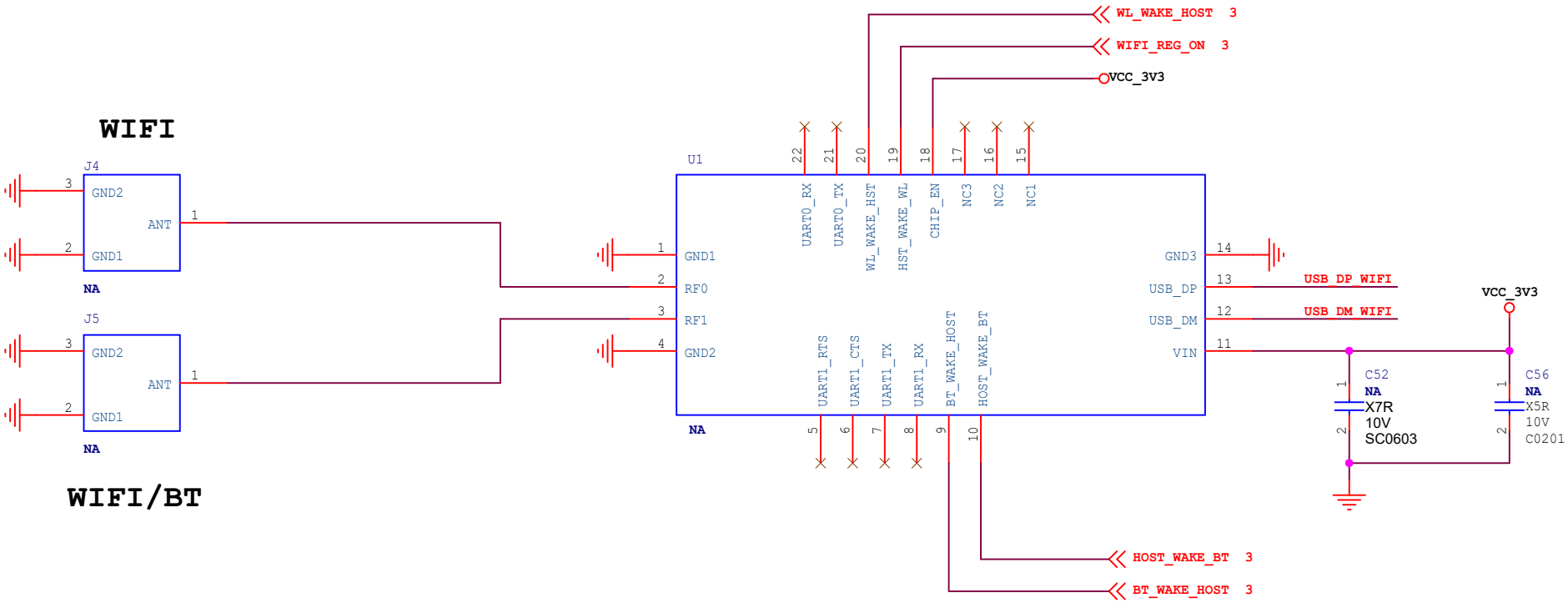

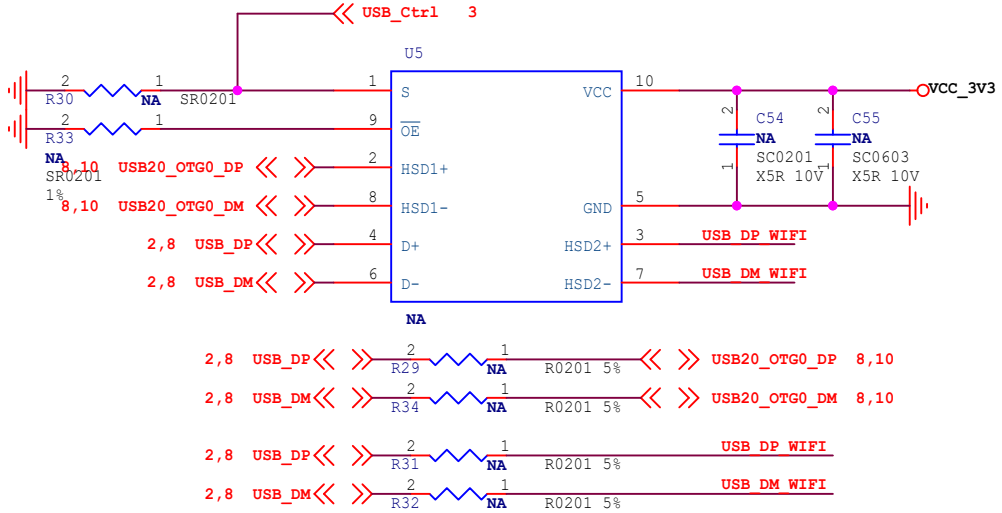

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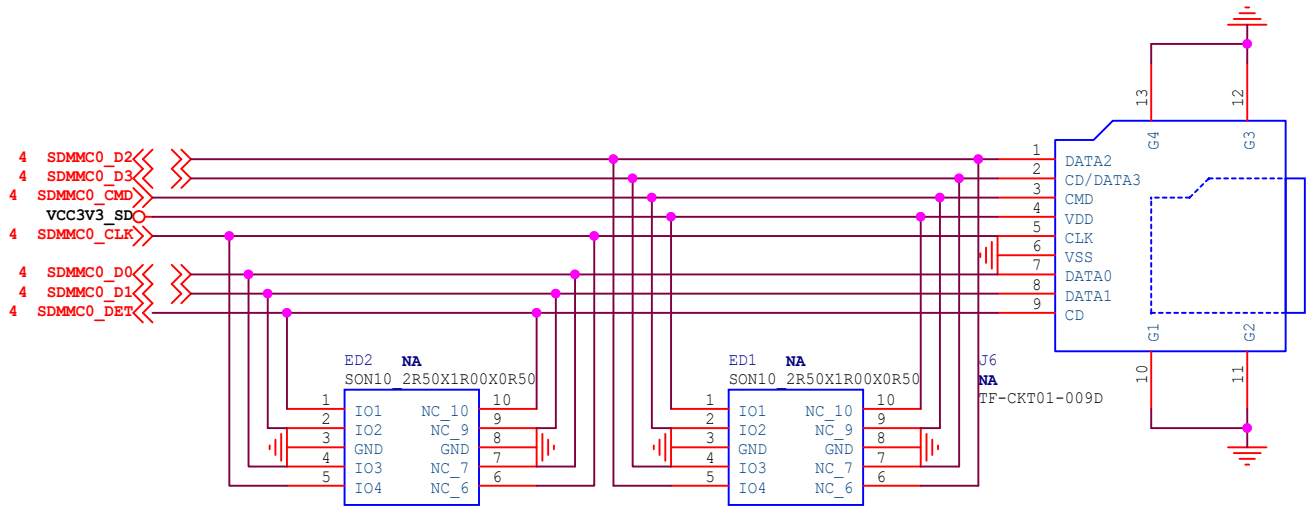
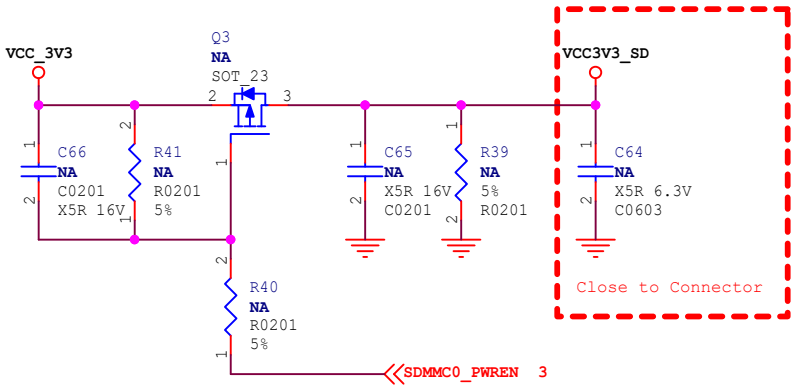
Micro-SD Card



Card Power



SDMMC0_PWREN=L VCC3V3_SD=3.3V(Default)
SDMMC0_PWREN=H VCC3V3_SD=0V

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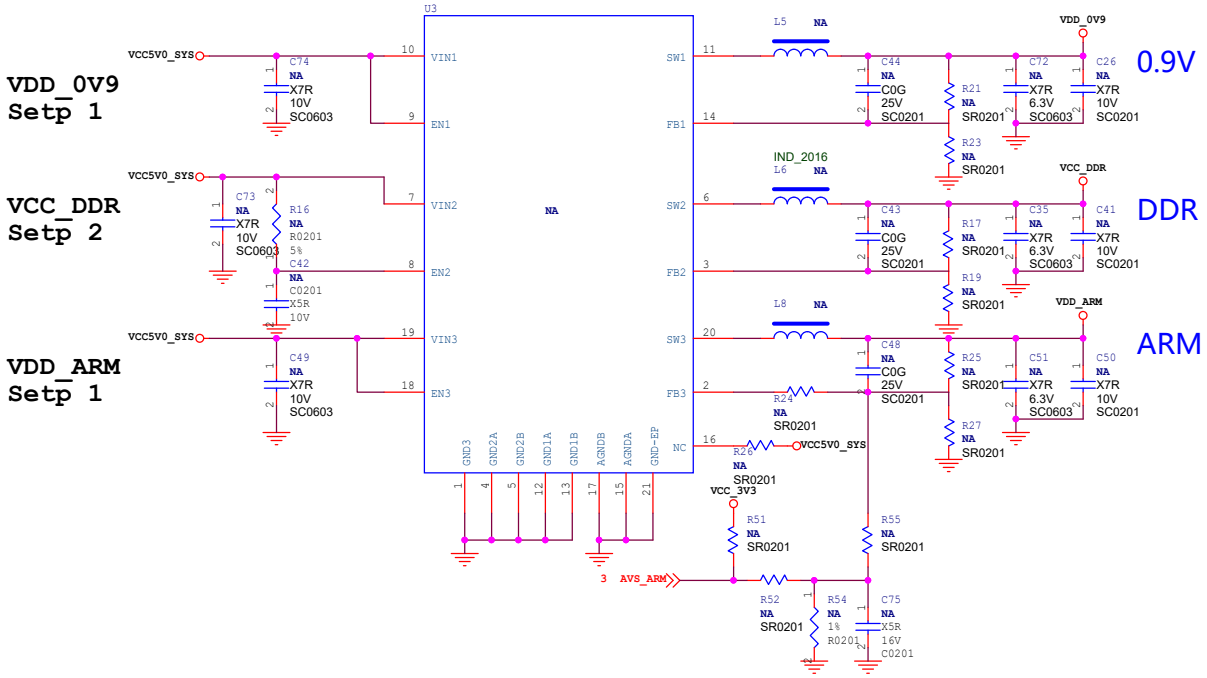
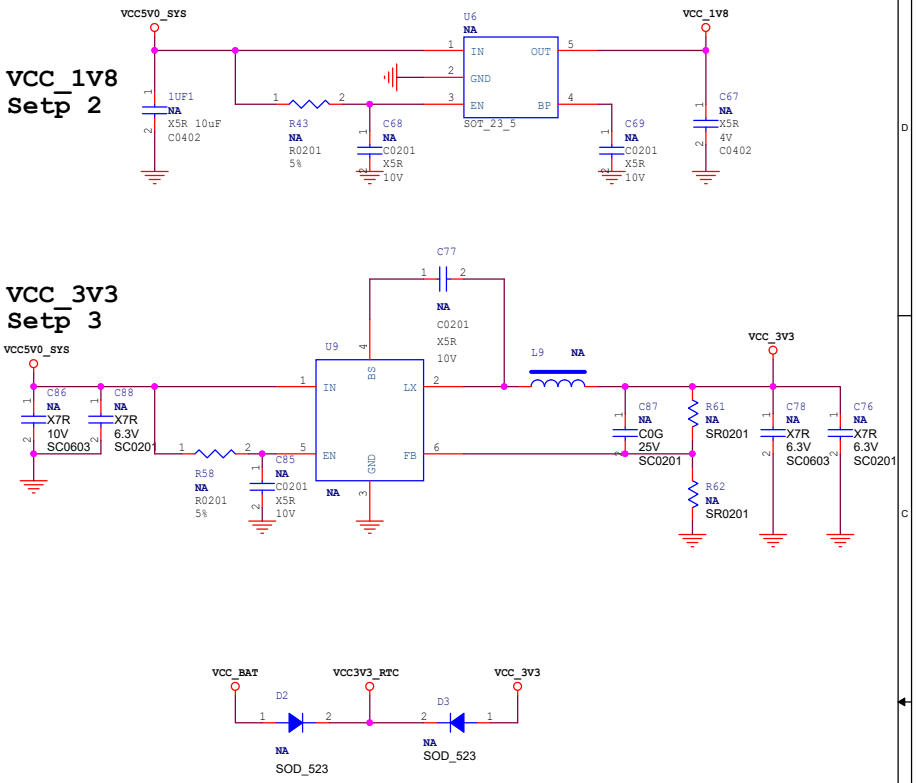
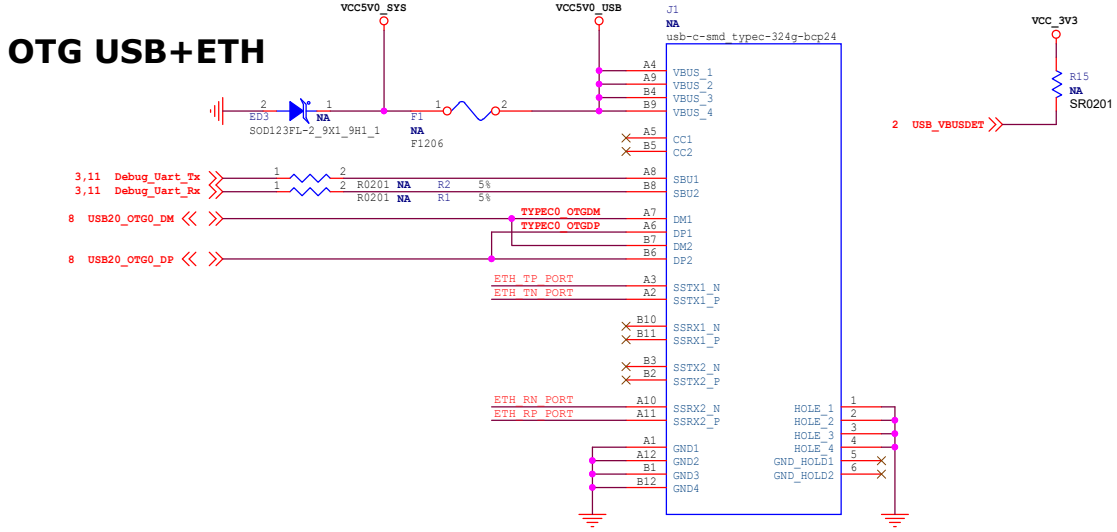
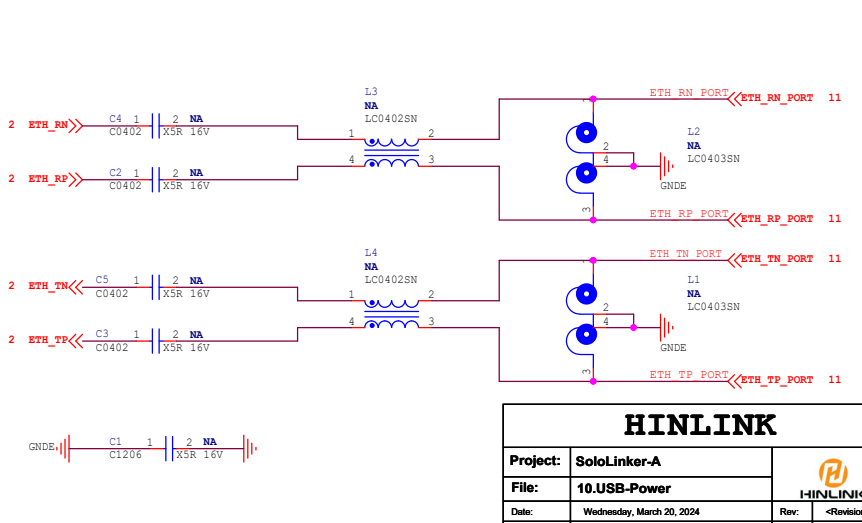
File:	09.SD Card		
Date:	Wednesday, March 20, 2024	Rev:	<Revision>
Designer:	<designer>	Sheet:	9 of 12









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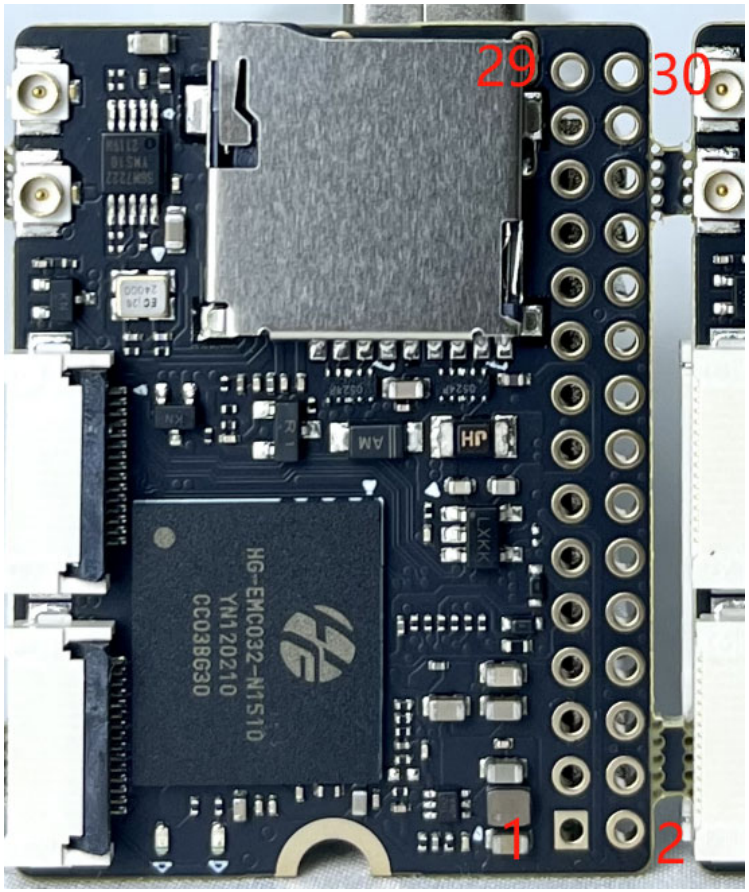

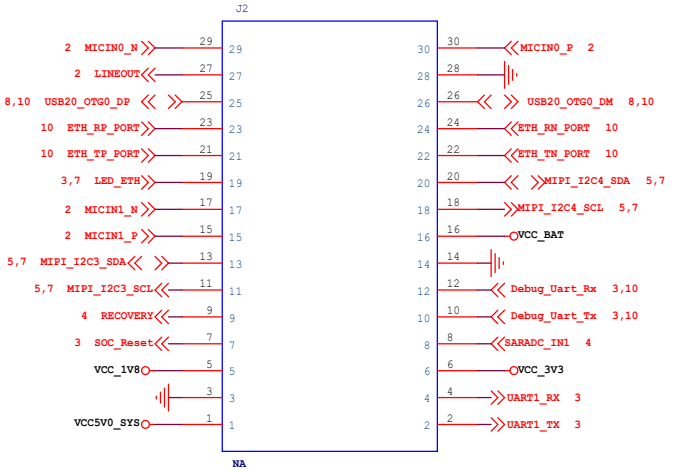

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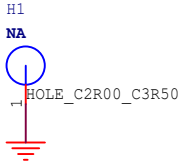
Date:	Wednesday, March 20, 2024	Rev:	<Revision>
Designer:	<designer>	Sheet:	11 of 12

HOLE



MARK

HINLINK			
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Date:	Wednesday, March 20, 2024	Rev:	<Revision>
Designer:	<designer>	Sheet:	12 of 12